FORM PTO-1449 (Modified) LIST OF PATENTS AND PUBLICATIONS PO				TTY. DOCKET NO. BUR920040075US1	SERIAL NO. Unassigned 10/109,361			
	ENTS AND PUBLICA INFORMATION DI		Rail	APPLICANT: Eric A. Foreman, et al.				
(Use several sheets if necessary)				FILING DATE: 04/2	GROUP: 2863 Unassigned			
REFERENCE D	ESIGNATION	U.S.	PATE	NT DOCUMENTS				
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	OTHE	R ART (Includia	ng Aut	hor, Title, Date, Pertine	nt Pages, e	tc.)		
TZ	Aseem Agarwal, et al., "Statistical Timing Analysis for Intra-Die Process Variations with Spatial Correlations," ICCAD '03, November 11-13, 2003, pages 621-625.							
TL	Hongliang Chang, et al., "Statistical Timing Analysis Considering Spatial Correlations Using a Single Pert-Like Traversal," ICCAD '03, November 11-13, 2003, pages 900-907.							
TL	Michael Orshansky, et al., "Impact of Spatial Intrachip Gate Length Variability on the Performance of High- Speed Digital Circuits," IBEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, Vol. 21, No. 5, May 2002, pages 544-553.							
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not in conformance and not considered. Include copy of this form with next communication to applicant.